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## Measurement of the quantum of thermal conductance

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The physics of mesoscopic electronic systems has been explored for more than 15 years. Mesoscopic phenomena in transport processes occur when the wavelength or the coherence length of the carriers becomes comparable to, or larger than, the sample dimensions. One striking result in this domain is the quantization of electrical conduction, observed in a quasi-one-dimensional constriction formed between reservoirs of two-dimensional electron gas<sup>1,2</sup>. The conductance of this system is determined by the number of participating quantum states or ‘channels’ within the constriction; in the ideal case, each spin-degenerate channel contributes a quantized unit of  $2e^2/h$  to the electrical conductance. It has been speculated that similar behaviour should be observable for thermal transport<sup>3,4</sup> in mesoscopic phonon systems. But experiments attempted in this regime have so far yielded inconclusive results<sup>5–9</sup>. Here we report the observation of a quantized limiting value for the thermal conductance,  $G_{th}$ , in suspended insulating nanostructures at very low temperatures. The behaviour we observe is consistent with predictions<sup>10,11</sup> for phonon transport in a ballistic, one-dimensional channel: at low temperatures,  $G_{th}$  approaches a maximum value of  $g_0 = \pi^2 k_B^2 T/3h$ , the universal quantum of thermal conductance.

The thermal conductance of phonon waveguides in the ballistic, one-dimensional limit has recently been calculated using the Landauer formula<sup>10–12</sup>. This approach, which bears a formal similarity to the model developed for radiative thermal transport in solids<sup>13</sup>, leads to an expression for the reservoir-to-reservoir heat current  $J_{th}$  that takes the form:

$$J_{th} = \sum_m \int_0^\infty \frac{dk}{2\pi} \hbar \omega_m(k) v_m(k) [\eta_{hot} - \eta_{cold}] T_m(k) \quad (1)$$

Here  $m$  is the mode index,  $\omega_m(k)$  is the phonon dispersion relation for wavevector  $k$ ,  $v_m(k)$  is the group velocity,  $\eta = [\exp(\hbar \omega_m/k_B T) - 1]^{-1}$  are Bose–Einstein occupation factors for the two (hot/cold) thermal reservoirs connected by the phonon

waveguides, and  $T_m(k)$  are transmission coefficients characterizing the coupling of waveguide modes to these reservoirs. As for the case with electrons, conversion to an integral over energy,  $\hbar \omega$ , leads to a cancellation between the density of states  $\partial k/\partial \omega_m$  and the modal group velocity,  $v_m(k)$ . In the limit of linear response,  $\Delta T \ll T$ , the resulting expression for thermal conductance depends only upon  $T_m(\omega)$ , the mode thresholds at  $q = 0$  (labelled  $\omega_m$ ), and fundamental constants:

$$G_{th} = \frac{J_{th}}{\Delta T} = \frac{k_B^2}{h} \sum_m \int_{x_m}^\infty dx \frac{x^2 e^x}{(e^x - 1)^2} T_m(x k_B T/\hbar) \quad (2)$$

Here,  $\Delta T$  is the small, steady-state temperature difference between reservoirs that sustains  $J_{th}$  and  $x_m = \hbar \omega_m(k=0)/k_B T$ .

This expression becomes greatly simplified in the limit  $k_B T \leq \hbar \omega_m$ , where only the four lowest lying, massless modes (that is, having zero threshold,  $\hbar \omega_m(k=0) = 0$ ) make an appreciable contribution. These four lowest modes arise from one dilatational, one torsional, and two flexural degrees of freedom<sup>14</sup>. For ideal coupling between the ballistic thermal conductor and the reservoirs, yielding modal transmission coefficients,  $T_m$ , equal to unity, a fundamental relation holds for each mode,

$$G_{th} = g_0 = \pi^2 k_B^2 T/(3h) \quad (3)$$

an expression devoid of any material parameters. This quantum of thermal conductance,  $g_0 = (9.456 \times 10^{-13} \text{ W/K}^2)T$ , represents the maximum possible value of energy transported per phonon mode. Surprisingly, it does not depend on particle statistics—but is universal for fermions, bosons and anyons<sup>15,16</sup>. A connection exists between the maximum thermal conductance and the maximum rate at which information, or entropy, can flow in a single quantum channel<sup>3,17</sup>.

Electrical and thermal conductance quantization are manifestly different in a number of ways; we discuss here two that are most obvious. The factor of  $T$  in equation (3), which gives the thermal conductance quantum a linear dependence upon temperature, reflects the fact that the quantity transported is energy (that is, entropy). In the case of electronic conduction, the corresponding quantity is the electron charge,  $e$ , and the electrical conduction quantum per spin-degenerate band is temperature independent,  $g_0^e = 2e^2/h$ . Also, in experiments with electrons, a series of conductance steps is observed<sup>1,2</sup>. In such experiments, chemical potential and temperature can be controlled separately. At a fixed, low temperature, the edge of the sharp electron distribution function can be swept sequentially through one-dimensional modes. With phonons, we can sweep only temperature; in this case, the broader Bose distribution smears out all features except for the final low-temperature plateau which occurs only when all but the lowest-lying modes become thermally depopulated<sup>10,11</sup>.

There are three essential criteria for observing the thermal conductance quantum. First, ballistic phonon transport through one-dimensional ‘waveguides’ is required. Second, as described below, coupling between the lowest modes and the thermal reservoirs must be optimal over an appreciable temperature range (their transmission coefficients must remain close to unity). Finally, the sample configuration and measurement technique must provide sufficient resolution to observe the contribution to  $G_{th}$  from a single mode.

To satisfy these criteria, we have developed new fabrication techniques based on the initial work of Tighe *et al.*<sup>18</sup> on thermally isolated mesoscopic samples with integrated transducers. Figure 1 shows a representative sample, which includes a phonon ‘cavity’ (a quasi-isolated thermal reservoir) suspended by four phonon ‘waveguides’. These are fabricated from a 60-nm-thick silicon nitride membrane by electron beam lithography and pattern transfer techniques described elsewhere<sup>19</sup>. In the earlier work, Tighe *et al.* fabricated devices from a GaAs layer that was monocrystalline. This

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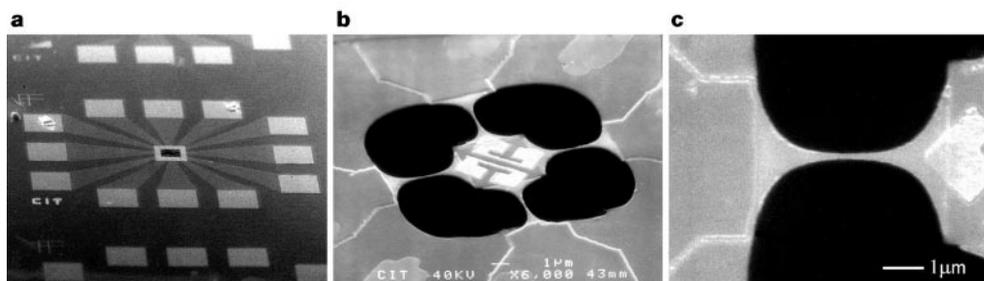
is not the case for the silicon nitride structural layer employed here; however, this material does provide a homogeneous effective medium for the long-wavelength phonons of relevance to these experiments. Recent measurements have shown that phonon propagation in this material is ballistic at low temperatures<sup>20,21</sup>.

Motivated by idealized scalar-wave calculations for an isotropic elastic solid of comparable geometry and dimensions<sup>11</sup>, we have patterned the width of the waveguides to follow  $\cosh^2(x/\lambda)$ , where  $x$  is the longitudinal coordinate and  $\lambda = 1.0 \mu\text{m}$ . The calculations, valid for longitudinal wave propagation through waveguides of this shape, indicate that quantized thermal conductance may be observable over an appreciable temperature range; this range is bounded at low temperatures by loss of adiabatic coupling to the reservoirs and, at higher temperatures, by the onset of thermal population of higher-energy (massive) modes.

Integration of carefully designed transducers allows thermal conductance measurements on the nanoscale phonon waveguides. We realize these by patterning (from a 25-nm-thick Au film) two thin-film resistors on top of the phonon cavity. As described below, these serve as a local heater and thermometer for the measurements;

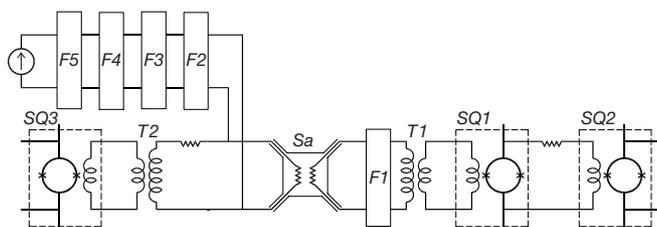
a second thermometer measures substrate temperature. Electrical connection to these transducers is provided by thin-film Nb leads (thickness 25 nm) patterned above the waveguides; these extend from the cavity to wirebond pads anchored to the main part of the chip. By virtue of their superconductivity, these leads do not provide a parasitic thermalization path to the reservoirs.

In principle, the measurement technique is simple—we first raise the temperature of the phonon cavity by Joule-heating its integrated Au resistor. We then employ the second transducer to measure the induced change in temperature. The ratio of heat current flowing through the waveguides to the induced rise in cavity temperature is the phonon thermal conductance of the waveguides. In practice, however, this measurement is quite challenging—it is necessary to obtain a sensitive measurement of phonon temperature within a suspended nanostructure that has very small heat capacity. The measurement process itself must involve minimal power dissipation, because exceedingly weak coupling exists between the sensor electrons and the cavity phonons at very low temperatures ( $10^{-13} \text{ W K}^{-1}$  at 100 mK for  $0.1 \mu\text{m}^3$  of Au)<sup>22</sup>. As depicted in Fig. 2, we employ d.c. SQUID-based noise thermometry<sup>23,24</sup> to measure the

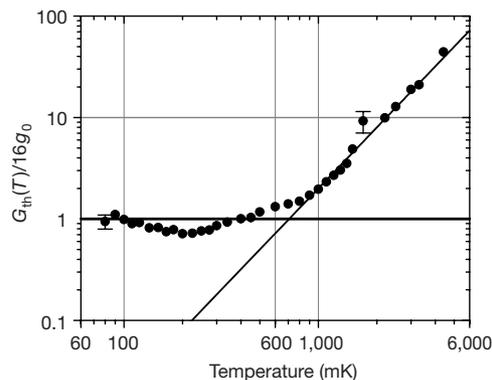


**Figure 1** Suspended mesoscopic device. A series of progressive magnifications are shown. **a**, Overall view of the  $\sim 1.0 \times 0.8 \text{ mm}$  device, showing 12 wirebond pads that converge via thin-film niobium leads into the centre of the device. This central region is a 60-nm-thick silicon nitride membrane, which appears dark in the electron micrograph. **b**, View of the suspended device, which consists of a  $4 \times 4 \mu\text{m}$  ‘phonon cavity’ (centre) patterned from the membrane. In this view, the bright ‘c’ shaped objects on the device

are thin-film gold transducers, whereas in the dark regions the membrane has been completely removed. The transducers are connected to thin-film niobium leads that run atop the ‘phonon waveguides’; these leads ultimately terminate at the wirebond pads. **c**, Close-up of one of the catenoidal waveguides, displaying the narrowest region which necks down to  $<200\text{-nm}$  width.



**Figure 2** Simplified apparatus diagram. The suspended sample (Sa), cooled on the dilution refrigerator’s mixing chamber (MC), is represented by two ( $\sim 20 \Omega$ ) resistors in the centre of the figure. Its rightmost (sensor) transducer is coupled to a d.c. SQUID-based noise thermometry circuit. Two thin-film d.c. SQUIDs (SQ1, SQ2) are cooled on the MC to enable ultralow noise performance: SQ1 is run open-loop (with continuous gain measurement carried out at 900 Hz, that is, just outside the noise measurement band) while its readout, SQ2, is operated in flux-locked mode. The metallic powder filter (F1) on the MC shields the sample from Josephson radiation emanating from the SQUIDs. The sample’s leftmost (heater) transducer is connected to a heavily filtered (F2–F5) current biasing circuit used to Joule-heat the phonon cavity. A SQUID voltmeter circuit (SQ3) on the MC allows resistance measurement *in situ*, by sensing the voltage drop across the current biased heater. The heavily shielded superconducting transformers (T1, T2) provide a current gain of  $\sim 18$ . The cold sample is isolated from heat conduction from the higher-temperature environment through filters F2 and F3 (cooled on the MC) and F4 and F5 (at 700 mK). F2 and F4 are metallic powder filters, while F3 and F5 are 10-pole RC filters.



**Figure 3** Thermal conductance data. We normalize the measured thermal conductance by the expected low-temperature value for 16 occupied modes,  $16 g_0$ . Measurement error is approximately the size of the data points, except where indicated. For temperatures above  $T_{co} \approx 0.8 \text{ K}$ , we observe a cubic power-law behaviour consistent with a mean free path of  $\sim 0.9 \mu\text{m}$ . For temperatures below  $T_{co}$ , we observe a saturation in  $G_{th}$  at a value near the expected quantum of thermal conductance. Consistent with expectations, to within experimental uncertainty,  $G_{th}$  never exceeds  $16 g_0$  once it finally drops below that value. The (implicit) linear temperature dependence observed for  $T < T_{co}$  is the hallmark of one-dimensional thermal transport. The approximate saturation at the maximum expected magnitude,  $G_{th} \approx 16 g_0$ , indicates that both the ballistic and adiabatic conditions have been satisfied in these experiments.

electron temperature within the Au film patterned on the suspended cavity. We also employ a second d.c. SQUID system to measure, *in situ*, the resistance of the transducer serving as a local heater. These d.c. SQUID devices operate with nearly quantum-limited energy sensitivity ( $\sim 220\text{h}$ )<sup>25</sup>, thus permitting measurement of temperature with extremely small back action, estimated to be  $\sim 10^{-20}\text{ W}$  or smaller.

To obtain a d.c. connection from the room-temperature electronics directly to the suspended nanostructures, we have installed an extensive array of filters within the cryostat (Fig. 2). These absorb both the environmental and the Johnson noise radiated from higher temperatures and Josephson radiation from the SQUIDS, and include cryogenically-cooled 10-pole RC-filters and “lossy-line” filters<sup>26</sup>. To illustrate the challenges involved, the total available power radiated from a resistor at 1 K to one at the (coldest) sample stage on the mixing chamber of the dilution refrigerator is  $\sim 10^{-12}\text{ W}$ , and contains significant spectral density from d.c. to frequencies of the order of 40 GHz. This is far above tolerable power levels ( $\sim 10^{-15}\text{ W}$ ) at 100 mK. The filters we have employed provide measured attenuation  $>200\text{ dB}$  from 1 MHz to 20 GHz. One set of these is installed at the still (0.7 K) to adsorb the power radiated down from 300 K, while the second bank is installed at the mixing chamber (coldest stage) to absorb the power radiated downward from the filters at 0.7 K. We estimate the maximum total power coupled to the device from room temperature is less than  $10^{-18}\text{ W}$ , even while maintaining a d.c. connection to the suspended nano-scale sample on the cold stage.

Noise thermometry is performed by integrating the noise current emanating from the sensor over a 500-Hz bandwidth centred at 1.25 kHz (Fig. 2). The measured noise power is directly proportional to temperature from  $>4\text{ K}$  down to  $\sim 80\text{ mK}$ . Below this, the transducer electrons lose contact with the lattice<sup>22</sup>, and the electrons remain at a temperature above that of the dilution refrigerator, presumably due to residual external noise. The observed saturation at low temperatures can be attributed to approximately  $10^{-16}\text{ W}$  of parasitic power input. This residual power is always very much smaller than the minimum level used to probe  $G_{\text{th}}$  in our experiments, which is of the order of  $10^{-14}\text{ W}$  at the lowest temperatures, and hence plays a negligible role. We note, however, that its magnitude is inconsistent with the measured attenuation of our filters, our carefully shielded environment, and the back action power of the SQUIDS. It is reminiscent of low temperature saturation in phase breaking times, and may have physical significance<sup>27</sup>.

Figure 3 shows the measured thermal conductance, normalized to  $16g_0$  (for reasons described below), which displays a clear saturation at low temperature. This is in striking contrast with the behaviour manifested above  $\sim 1\text{ K}$  where a precipitous rise in  $G_{\text{th}}$ , closely following a cubic power law, is manifested. Roughly speaking, with decreasing temperature the one-dimensional limit is attained when the thermal wavevector  $k_{\text{th}} = k_{\text{B}}T/(\hbar v)$  becomes smaller than the spacing between the lowest-lying modes,  $\Delta k \sim \pi/w$ . Assuming an average speed of sound  $v \sim 6,000\text{ m s}^{-1}$ , depopulation of the higher-lying, massive modes should occur below a crossover temperature  $T_{\text{co}} \sim \pi\hbar v/(k_{\text{B}}w) = 0.8\text{ K}$  for our waveguides of width  $w \approx 200\text{ nm}$ . Thus, with the phonon cavity cooled via four waveguides that are each expected to carry just four populated modes at the lowest temperatures,  $G_{\text{th}}$  should approach a limiting value of  $16g_0$  for  $T \ll T_{\text{co}}$ , as is found. Some structure is evident as temperature is decreased below 1 K but, significantly, once  $g$  becomes smaller than  $16g_0$ , to within experimental uncertainty, the data never exceed this value again. In this regime,  $16g_0$  represents the maximum theoretical conductance; it can be attained only with perfect coupling to the reservoirs. Deviations from exact quantization here are reminiscent of those generic to the case of electrical conduction quantization which are ubiquitous, and can be induced by disorder, non-adiabatic coupling to the reservoirs, and scattering at the threshold of transmission to higher channels<sup>28,29</sup>. In the present case, they may

also arise from the transition from specular to diffuse phonon scattering at the waveguide surfaces that is expected at elevated temperatures<sup>18</sup>. Additional experiments are currently underway to clarify these details.

These devices offer prospects for extremely sensitive calorimetry. We estimate that their total heat capacity at 100 mK, dominated by the electronic contribution from Au transducers, is of the order of  $C_{\text{tot}} \approx 10^3 k_{\text{B}}$ . We are currently attempting to measure  $C_{\text{tot}}$  directly. Our future investigations will focus upon nanocalorimeters with electron-gas transducers patterned from an  $n(+)$  GaAs epilayer, rather than thin-film Au as in this work. This may allow us to obtain values of  $C_{\text{tot}}$  of a few times Boltzmann's at 10 mK. Reading out such devices with the d.c. SQUID techniques developed for this work would yield energy sensitivity sufficient to count individual thermal phonons in the 10–100 mK regime. This level of sensitivity should allow access to a regime where it may be possible to observe the particle nature of the phonons; that is, quantum phonon optics<sup>30</sup>. □

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**Coulomb-blockade transport in single-crystal organic thin-film transistors**

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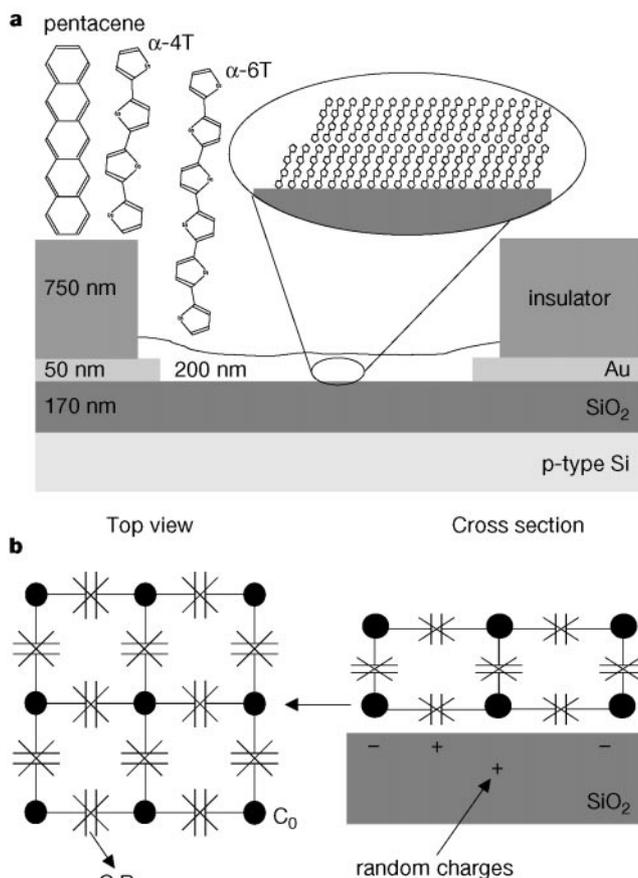
Coulomb-blockade transport—whereby the Coulomb interaction between electrons can prohibit their transport around a circuit—occurs in systems in which both the tunnel resistance,  $R_T$ , between neighbouring sites is large ( $\gg h/e^2$ ) and the charging energy,  $E_C$  ( $E_C = e^2/2C$ , where  $C$  is the capacitance of the site), of an excess electron on a site is large compared to  $kT$ . (Here  $e$  is the charge of an electron,  $k$  is Boltzmann's constant, and  $h$  is Planck's constant.) The nature of the individual sites—metallic, superconducting, semiconducting or quantum dot—is to first order irrelevant for this phenomenon to be observed<sup>1</sup>. Coulomb blockade has also been observed in two-dimensional arrays of normal-metal tunnel junctions<sup>2</sup>, but the relatively large capacitances of these micrometre-sized metal islands results in a small charging energy, and so the effect can be seen only at extremely low temperatures. Here we demonstrate that organic thin-film transistors based on highly ordered molecular materials can, to first order, also be considered as an array of sites separated by tunnel resistances. And as a result of the sub-nanometre sizes of the sites (the individual molecules), and hence their small capacitances, the charging energy dominates at room temperature. Conductivity measurements as a function of both gate bias and temperature reveal the presence of thermally activated transport, consistent with the conventional model of Coulomb blockade.

The experimental transport data available in the literature<sup>3–8</sup>, obtained with highly ordered polycrystalline thin-film transistors (TFTs) based on  $\alpha$ -6T and pentacene (see Fig. 1a), have so far been interpreted in terms of hopping between localized trap states, polaron hopping (both correlated and uncorrelated) or in terms of the Holstein model. However, large variations in the experimental data—on even nominally identical samples—limit the understanding of the intrinsic charge transport mechanisms of these systems.

Our sample layout is shown in Fig. 1a. The substrate is a highly doped silicon wafer, acting as a gate electrode, which is thermally oxidized in a dry atmosphere. The gold source and drain electrodes are lithographically defined, with gap diameters ranging from 2 to

20  $\mu\text{m}$ . A second isolation layer is deposited to isolate the transistor electrically from neighbouring devices. The organic materials used were pentacene (Aldrich), quaterthiophene ( $\alpha$ -4T; synthesized by Syncom BV) and sexithiophene ( $\alpha$ -6T; also from Syncom BV). These materials were purified, and deposited in the last fabrication step by thermal evaporation in a high-vacuum environment ( $1 \times 10^{-7}$  mbar). Large individual crystallites within a polycrystalline thin film are obtained by optimization of the substrate temperature and evaporation rate<sup>9,10</sup>. With optimal settings the individual crystallites can grow up to 40  $\mu\text{m}$  in diameter, which is large enough to fill one gap completely, resulting in a single-crystal TFT<sup>11</sup>. The identification, orientation and thickness of the single crystals can be obtained by an optical polarization microscopy technique<sup>9</sup>.

Room-temperature current–voltage ( $I$ – $V$ ) characteristics of a crystal pentacene TFT with a thin-film phase<sup>10</sup>, measured with increasing and decreasing drain voltage, are shown in Fig. 2a. All measurements are performed under high-vacuum conditions, and as a result show little or no hysteresis in the drain voltage sweep. In contrast to what is generally reported in the literature, we typically observe non-ohmic behaviour of the gold source and drain contacts to the organic active layer<sup>11</sup>, as is demonstrated at low drain bias in Fig. 2. The conductivity is obtained in the linear transport regime as a function of gate bias<sup>12</sup>, and corrected for the non-ohmic contacts. A typical result is shown in Fig. 2b. The non-ohmic effects of the contacts were compensated for by measuring several sweeps of gate voltage with increasing source–drain bias ( $V_d = -4, -5, -6$  and  $-7$  V). The channel conductance at a set gate voltage is then obtained from the slope of the channel current with the applied



**Figure 1** Schematic sample layout. **a**, Single-crystal TFT sample structure for  $\alpha$ -4T,  $\alpha$ -6T and pentacene materials. **b**, A schematic view of two molecular layers on top of the  $\text{SiO}_2$  gate-dielectric with random charges incorporated. In the in-plane direction, the layers are represented by a two-dimensional array of molecules separated by a tunnel junction resistance  $R_T$  with nearest-neighbour capacitance  $C$ . The self-capacitance molecule is given by  $C_0$ .