

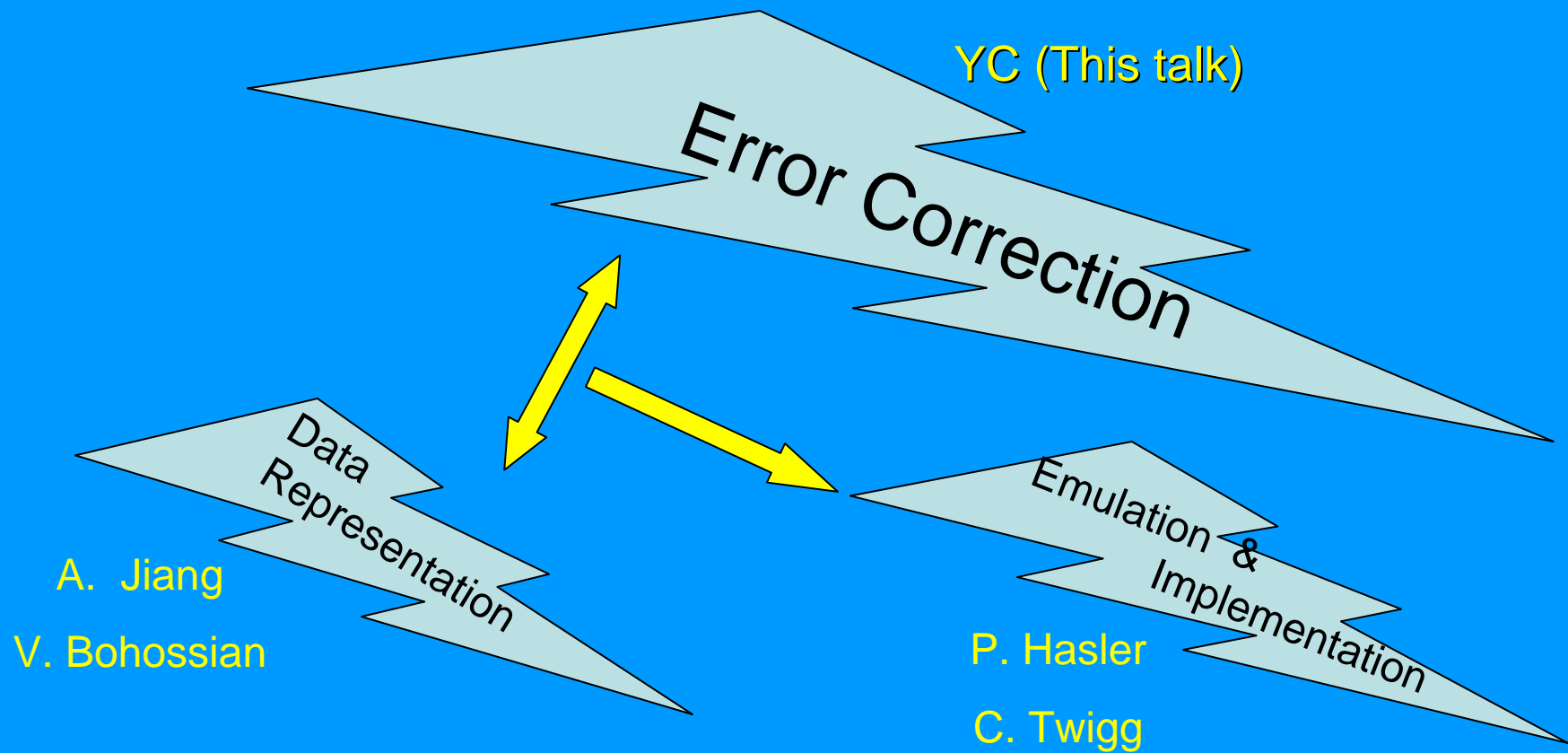
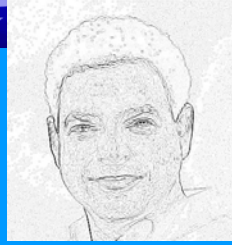
Coding for Denser and Faster Flash Memories



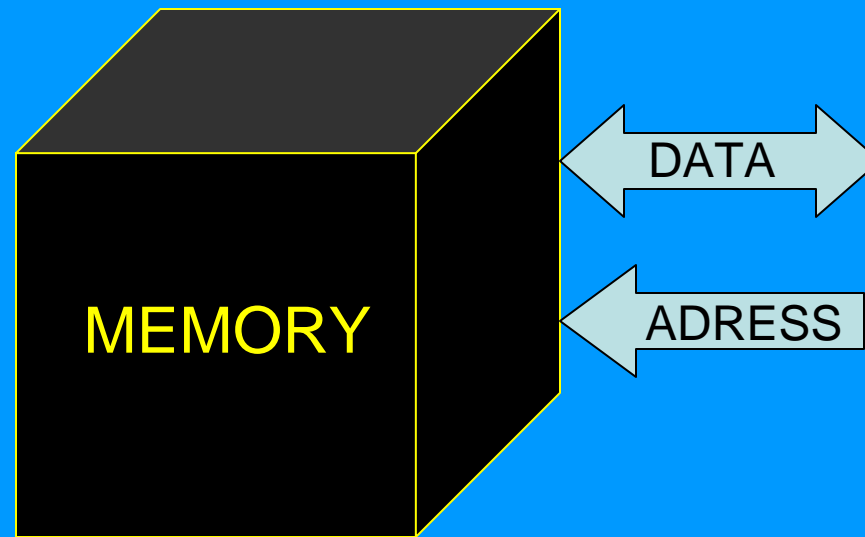
Yuval Cassuto
Paradise Laboratory
EE Department
Caltech



The Caltech Flash Project



Memory Devices

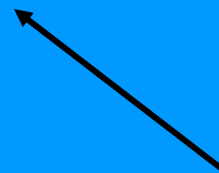


Functions

- Write DATA to ADRESS
- Read DATA from ADRESS

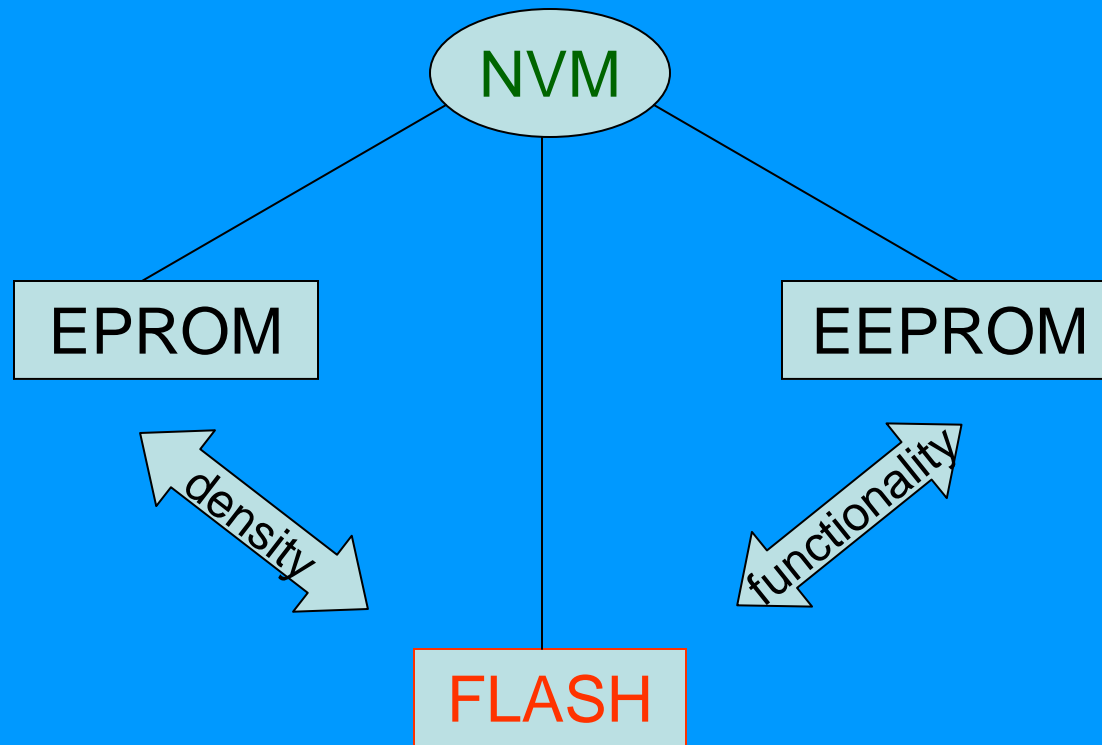
Characterizations

- Density (Capacity/Volume)
- Speed
- Reliability
- Functionality

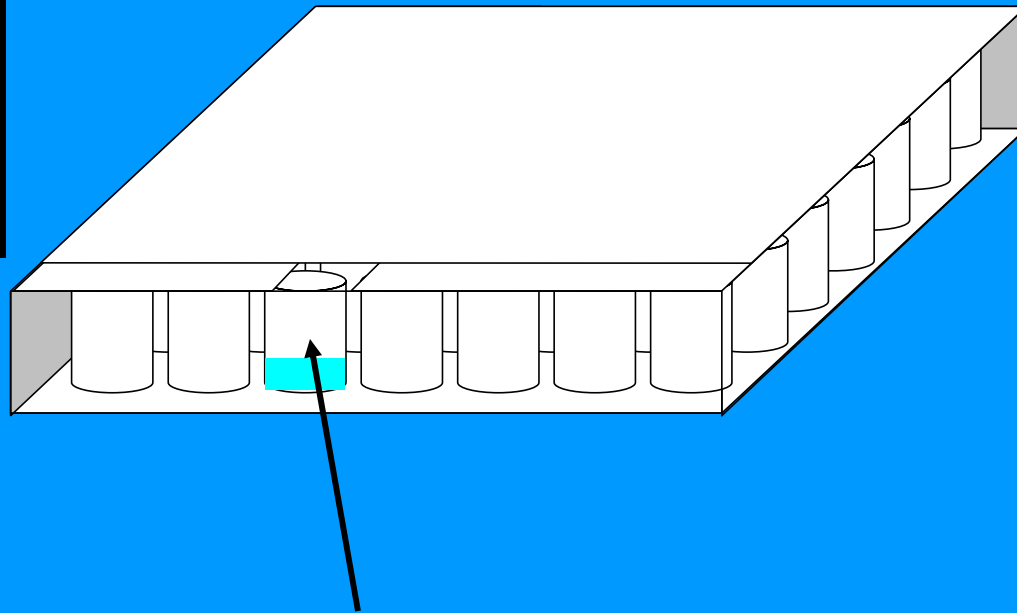
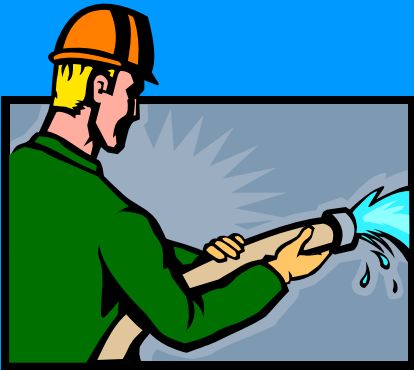


Flash Memory

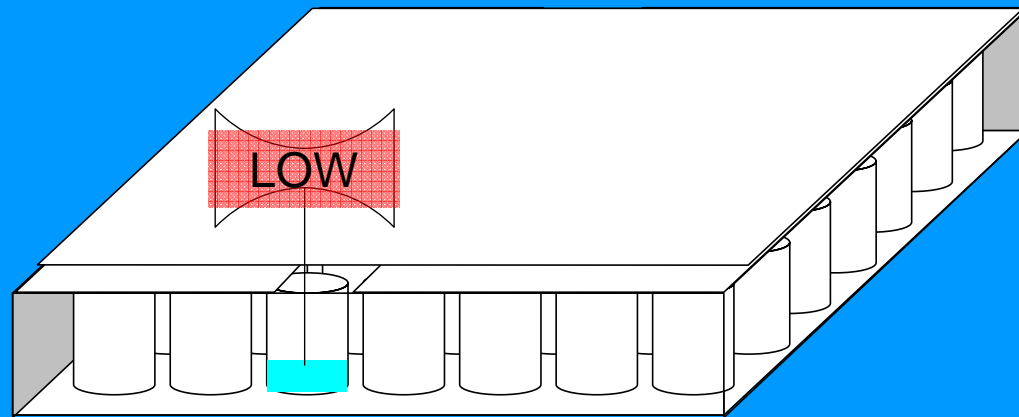
Flash is a Non-Volatile Memory Technology
(1979)



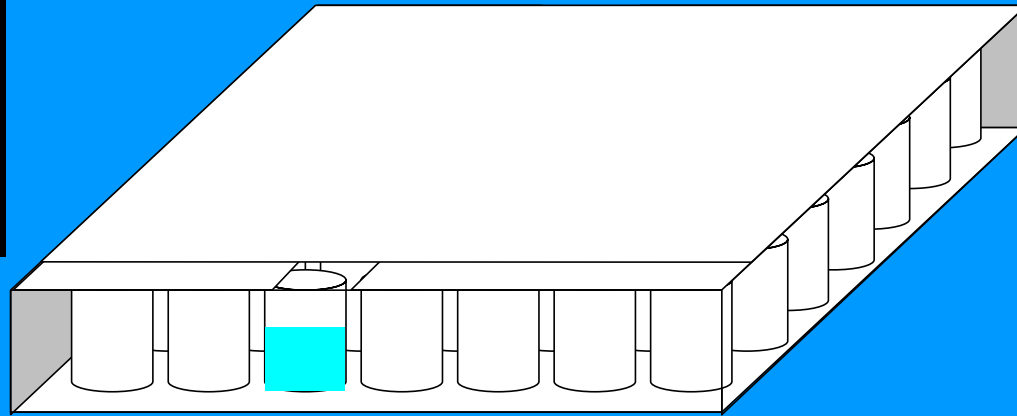
Flash Program Principles



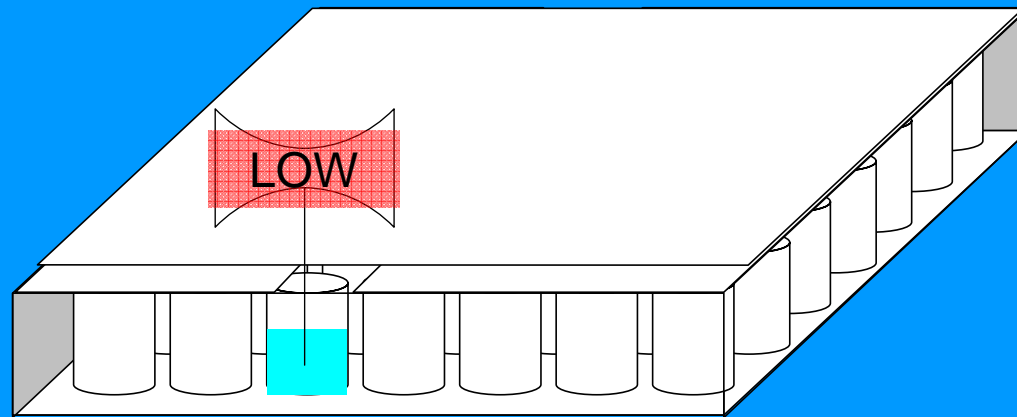
Flash Iterative Programming



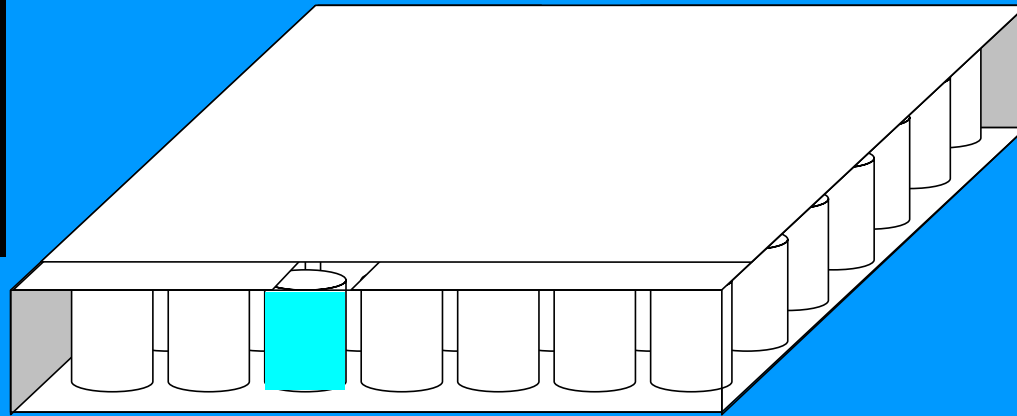
Flash Iterative Programming



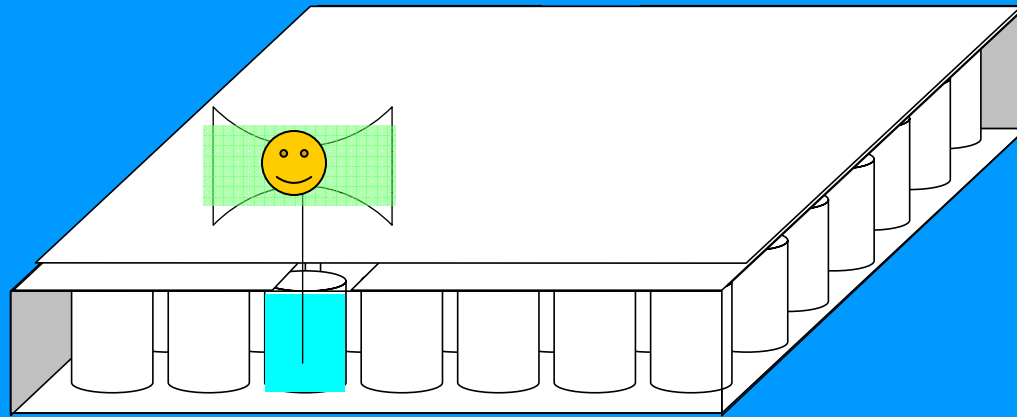
Flash Iterative Programming



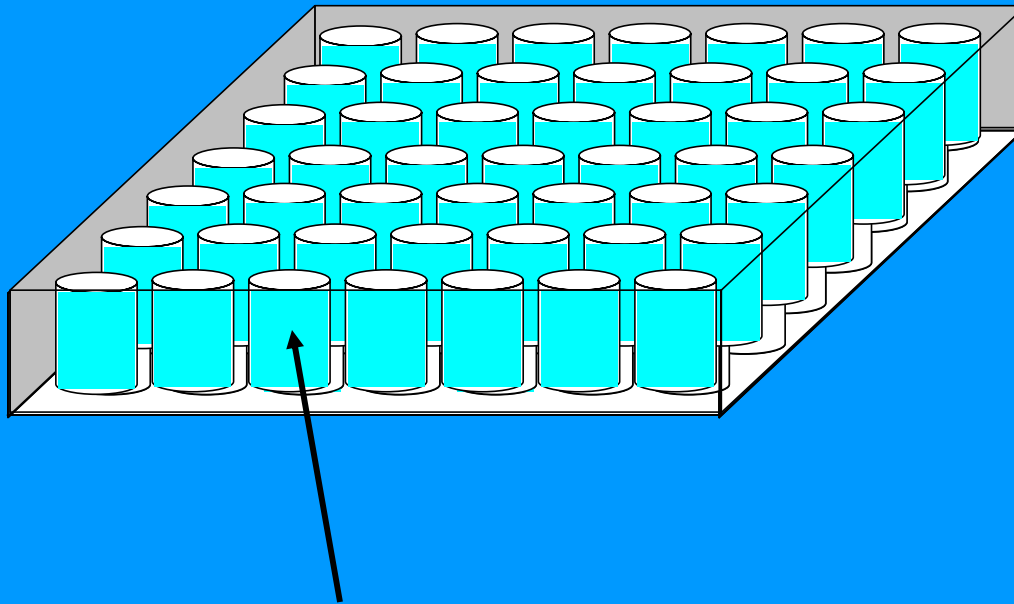
Flash Iterative Programming



Flash Iterative Programming



Flash Block Erase



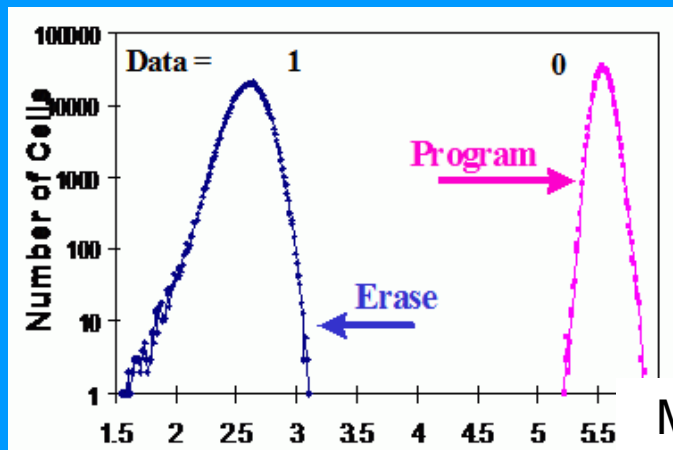
Flash Essentials

Method : Charge placement on Floating Gates

- Iterative programming
- Reliability issues
 - Disturbs
 - Retention
 - Endurance
- Block erase

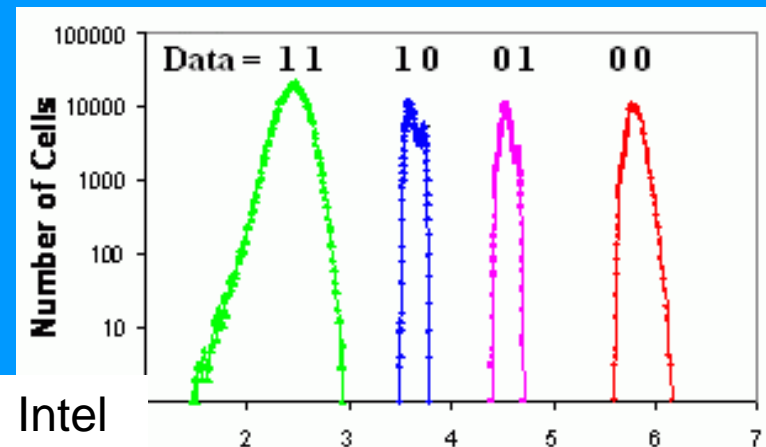
Multi-Level Flash Cell

2-Level cell



M. Bower, Intel

Multi-Level cell



- Iterative programming
- Reliability issues
 - Disturbs
 - Retention
 - Endurance

- Iterative programming
- Reliability issues
 - Disturbs
 - Retention
 - Endurance

Flash and ECC



Flash File Systems Overview

2006

4.2 Error Code Correction (ECC)

Error Code Correction (ECC) is a way to identify and correct errors during read or write operations to flash. ECC is very common in NAND flash due to NAND reliability issues. Most ECC for flash can detect and correct single bit errors. However as NAND, and some NOR, devices trend toward Multi-Level (MLC) architecture and smaller lithographies, there is a need to perform multiple bit correction as error rates increase.

ECC is generally performed within a memory controller although software ECC is also possible. Several ECC algorithms are available, including Hamming Code, BCH (Bose, Chaudhuri, Hocquenghem), and Reed-Solomon - three that are among the most popular. ECC algorithms vary in complexity and their impact on design cost.

An Information Theoretic View of MLC Flash

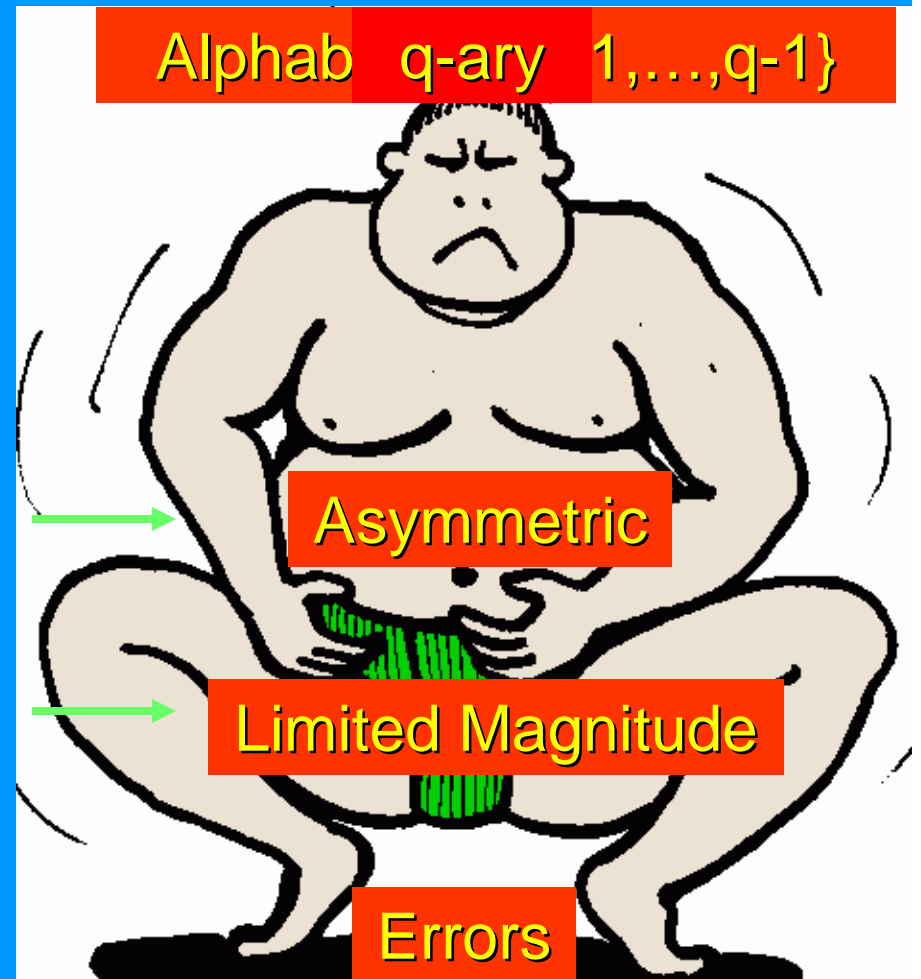
q-level cells



Alphabet q -ary $\{1, \dots, q-1\}$

- Disturbs
- Retention
- Endurance

Move cells in a
Dominant direction
To
Proximate levels



Asymmetric | Limited-Magnitude Errors

$$Q = \{0, 1, \dots, q - 1\}$$



Asymmetric I-limited-magnitude error:

$$0 \leq y - x \leq \ell$$

Asymmetric I-limited-magnitude error
with wrap around:

$$(y - x) \in \{0, 1, \dots, \ell\} \text{ mod } q$$

Codes for AIM Errors

Research Problem:

Code $C \subset Q^n$ that corrects t AIM errors

For each (n, ℓ, t)

Known Result: [Ahlsvede et.al '06]

Optimal (n, ℓ, n) codes

Trivial construction : code has all the words of L^n , where $L \subset Q$

Analyzing Codes for AIM Errors

AIM “distance” for code analysis:

For a pair of vectors $\mathbf{x} \in Q^n, \mathbf{z} \in Q^n$

$$d_\ell(\mathbf{x}, \mathbf{z}) = \begin{cases} n + 1 & \text{if } \max_i(|x_i - z_i|) > \ell \\ \max(N(\mathbf{x}, \mathbf{z}), N(\mathbf{z}, \mathbf{x})) & \text{otherwise} \end{cases}$$

$$N(\mathbf{x}, \mathbf{z}) = |\{i : x_i > z_i\}| \quad N(\mathbf{z}, \mathbf{x}) = |\{i : z_i > x_i\}|$$

Theorem

$C \subset Q^n$ corrects t AIM errors \Leftrightarrow

$$d_\ell(\mathbf{x}, \mathbf{z}) \geq t + 1 \text{ for all distinct } \mathbf{x}, \mathbf{z} \text{ in } C.$$

Example: Correcting A1M Errors

8	2	4	2	0
---	---	---	---	---

A1M ↓

A1M ↓

8	3	4	2	1
---	---	---	---	---

7	3	3	1	1
---	---	---	---	---

A1M ↓

A1M ↓

8	3	4	1	1
---	---	---	---	---

$n = 5, t = 2, l = 1$

8	③	4	2	①
---	---	---	---	---

Majority Even/Odd

⑧	3	④	1	1
---	---	---	---	---

8	2	4	2	0
---	---	---	---	---

-1 for all minority symbols

7	3	3	1	1
---	---	---	---	---

Construction 1

Given $\Sigma \subset Q'^n$ $Q' = \{0, \dots, q' - 1\}$, $\ell < q' < q$

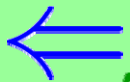
$$C = \{x \in Q^n : x \bmod q' \in \Sigma\}$$

Theorem

Σ corrects t $A\ell M$ errors with wrap-around



C corrects t $A\ell M$ errors



$$q \geq q' + \ell$$

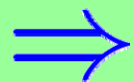
Construction 1': $q' = \ell + 1$

Given $\Sigma \subset Q'^n$ $Q' = \{0, \dots, \ell\}$, $\ell < q$

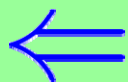
$$C = \{x \in Q^n : x \bmod (\ell + 1) \in \Sigma\}$$

Theorem

Σ corrects t symmetric errors



C corrects t AℓM errors



$$q > 2\ell$$

Construction 1: Proof

If $\mathbf{x} \in C, \mathbf{z} \in C$ come from the same codeword of Σ

$$d_\ell(\mathbf{x}, \mathbf{z}) = \begin{cases} n+1 & \text{if } \max_i (|x_i - z_i|) > \ell \\ \max(N(\mathbf{x}, \mathbf{z}), N(\mathbf{z}, \mathbf{x})) & \text{otherwise} \end{cases}$$

If $\mathbf{x} \in C, \mathbf{z} \in C$ come from different codewords of Σ

$$d_\ell(\mathbf{x}, \mathbf{z}) = \begin{cases} n+1 & \text{if } \max_i (|x_i - z_i|) > \ell \\ \max(N(\mathbf{x}, \mathbf{z}), N(\mathbf{z}, \mathbf{x})) & \text{otherwise} \end{cases}$$

Construction 1: Perfect Codes

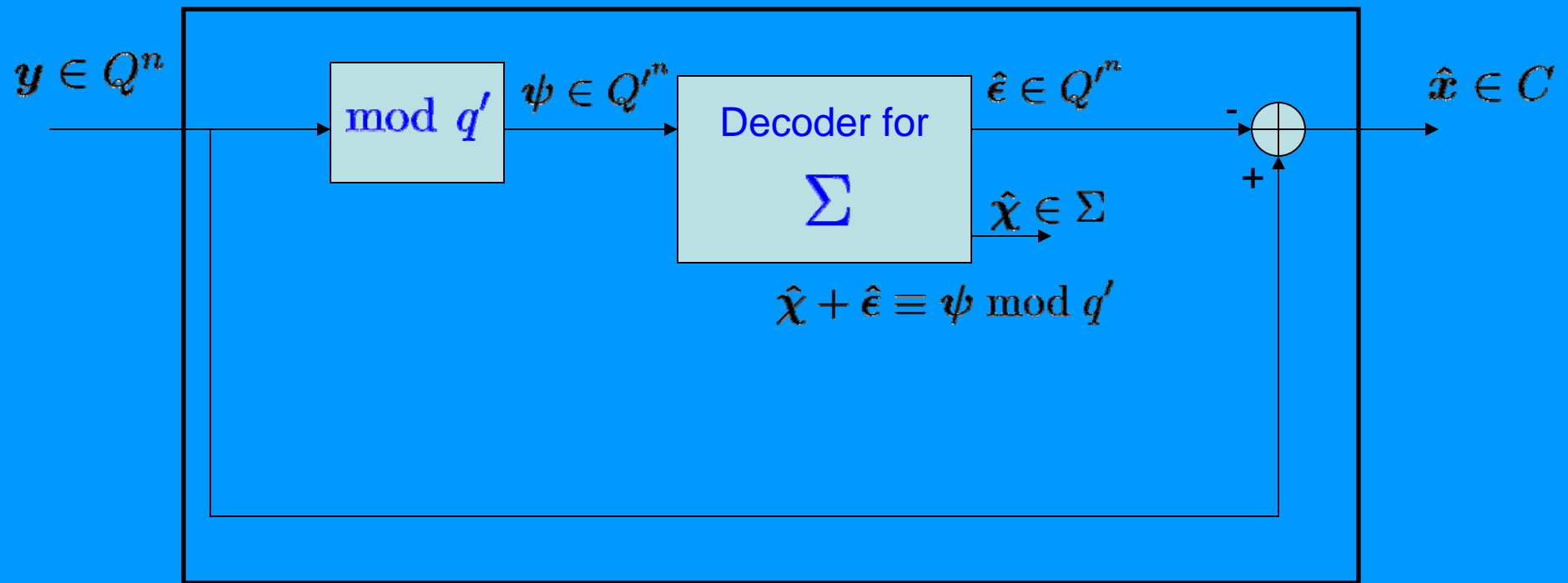
If Σ is a “perfect” code for **symmetric** errors, then
 C is a “perfect” code for **ALM** errors

AIM Sphere packing bound is met with \equiv

$$|C| \cdot \sum_{i=0}^t \binom{n}{i} \ell^i \leq q^n$$

↓

Construction 1: Decoding

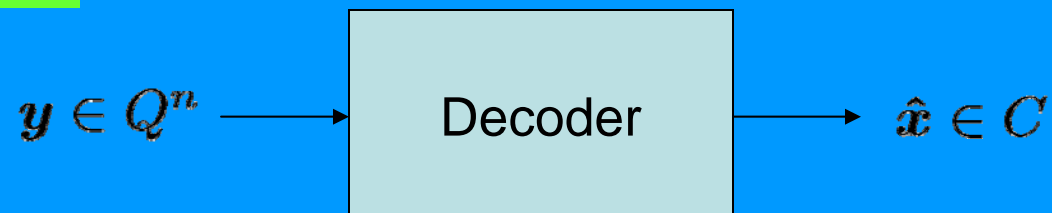


Something is Missing

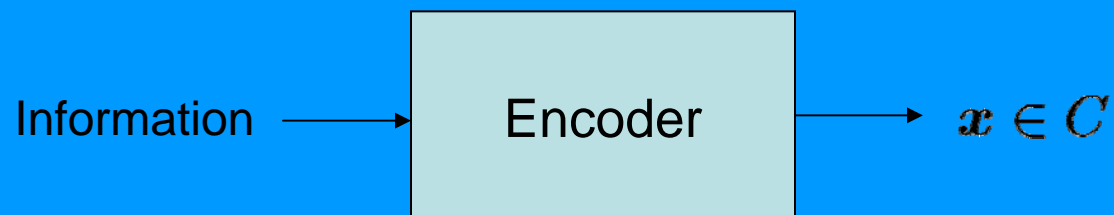
We know

$$\mathcal{C} \subset \mathbb{Q}^n$$

We have



But still need



Encoding Example

18 information bits

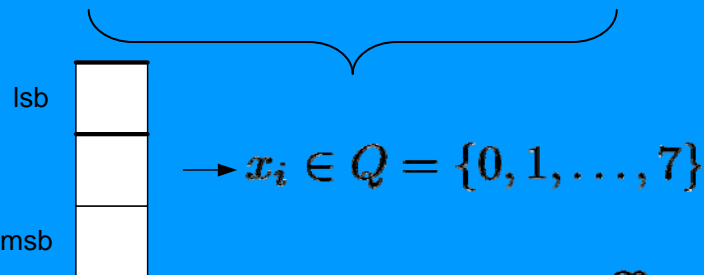


$q = 8$ Flash cells with $t = 1, \ell = 1$

0	1	1	0				
0	0	1	1	1	0	1	
1	1	0	1	0	1	1	

0	1	1	0	0	1	1
0	0	1	1	1	0	1
1	1	0	1	0	1	1

$\in \Sigma_H$



$\mathbf{x} =$

4	5	3	6	2	5	7
---	---	---	---	---	---	---

 $\in C$

Symbol Mappings

0	1	1	0	0	1	1	$\in \Sigma_H$
0	0	1	1	1	0	1	
1	1	0	1	0	1	1	

lsb

$\rightarrow x_i \in Q = \{0, 1, \dots, 7\}$

msb

$b_0 + 2 \cdot \text{Gray}(b_1, b_2)$

0	000	0
1	001	1
2	010	2
3	011	3
6	100	4
7	101	5
4	110	6
5	111	7

$$\sum_{j=0}^2 b_j 2^j$$

$\mathbf{x} =$

6	7	3	4	2	7	5
---	---	---	---	---	---	---

 $\in \mathcal{C}$

$\mathbf{x} =$

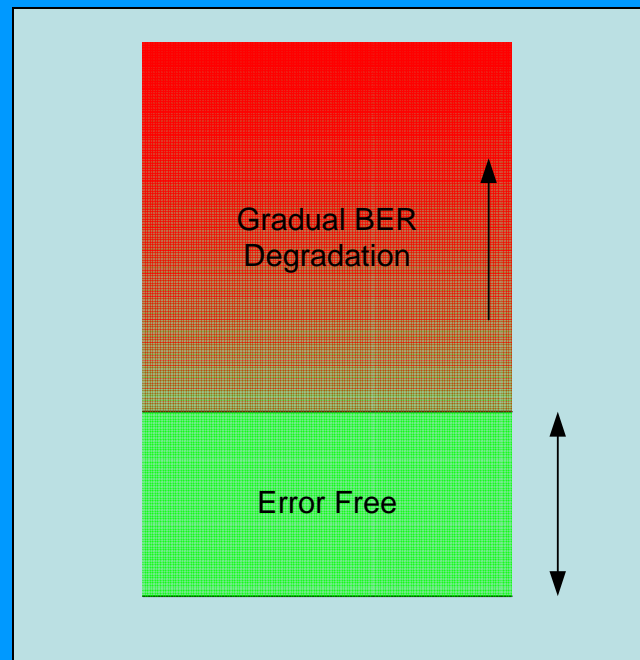
4	5	3	6	2	5	7
---	---	---	---	---	---	---

 $\in \mathcal{C}$

Symbol Mappings

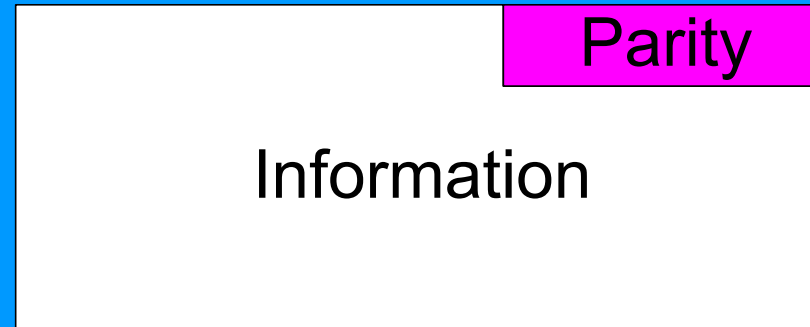
- The same **code** can be obtained by different symbol mappings
- Clever **mappings** can be used to reduce the Bit-Error Probability

Coding + Mapping = 2-phase Error Protection:



AIM Codes in Memory Devices

1) Encoding of Σ :



2) Mapping to Q^n :

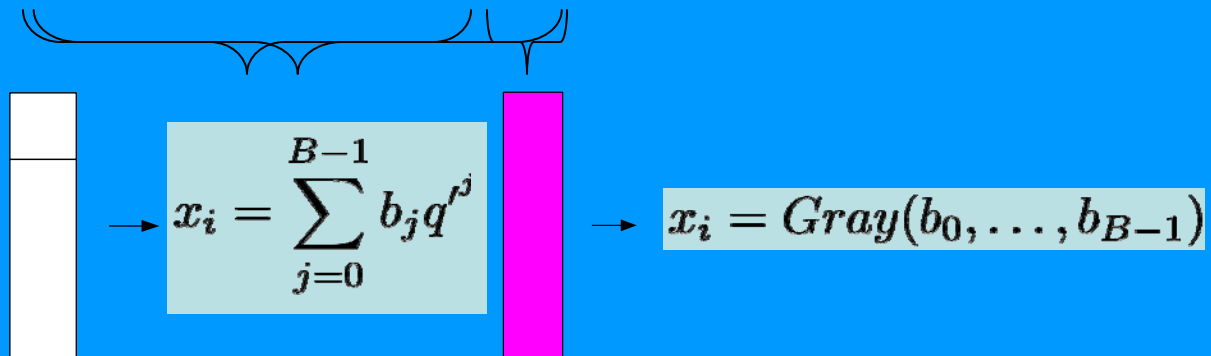
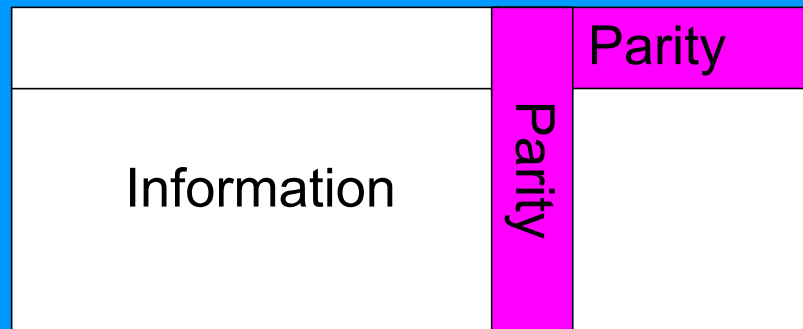


In Memory devices



Provides faster reads and updates

Systematic AIM Codes



Works for $l=1$

An AIM error in a parity symbol



One bit error for Σ

Mappings for Systematic AIM Codes

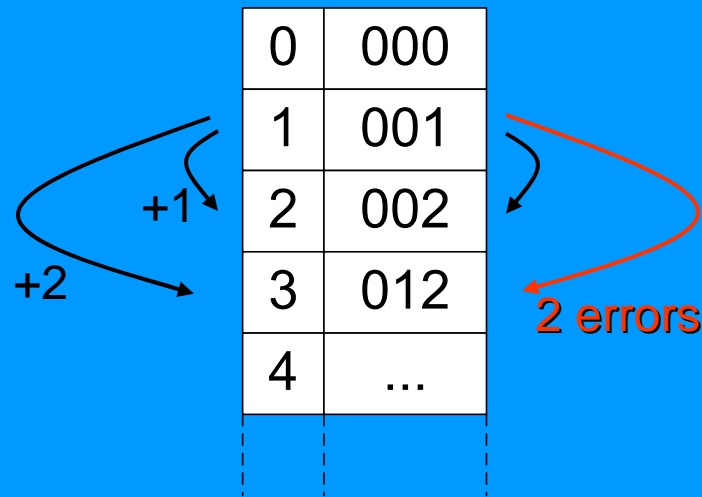
For $l > 1$

An AIM error in a parity symbol



One symbol error for Σ

Example: $q' = 3, \ell = 2$



Mappings for Systematic AIM Codes

For general q' and ℓ , how many worst-case Q' symbol errors per an AIM error?

At least 2 (for any mapping)

At most 2 (for some mappings):

Lemma

For any q', ℓ , the q' -ary Generalized Reflected Gray Code induces at most two Q' symbol errors for any AIM error.

- One symbol error is always at a fixed location
- The other error is always ± 1 modulo q'

Mappings for Systematic AIM Codes

Research Problem:

Find high rate (<1) mappings that induce only a single Q' error

$$|\tilde{Q}| = \frac{1}{2}|Q| = \frac{1}{2}q$$

$$R \triangleq \log_q |\tilde{Q}|$$



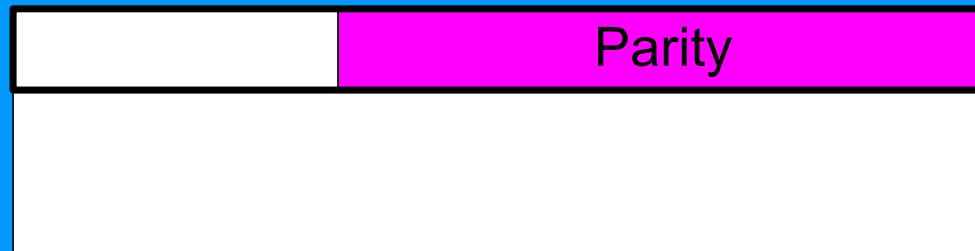
$$R = 1 - \frac{1}{\log_2 q}$$

	$\leftarrow B \rightarrow$
24	110
25	111
26	112
27	113
28	103
29	102
30	101
31	100
32	200
33	201
34	202
35	203
36	213
37	212
38	211
39	210

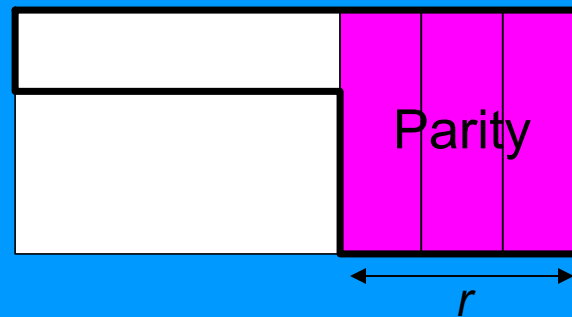
$$q = (q')^B$$

Construction 2

Construction 1



Folding parity symbols
(with r bits extra redundancy)



Lengthening code
(when Σ is linear)





Coding for enser and Faster Flash Memories



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Programming with Controlled Errors

- Disturbs
- Retention
- Endurance

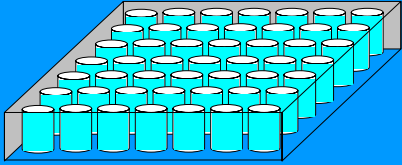
Uncontrolled AIM Errors (Read Errors)

- Iterative Programming

Controlled AIM Errors (Write Errors)

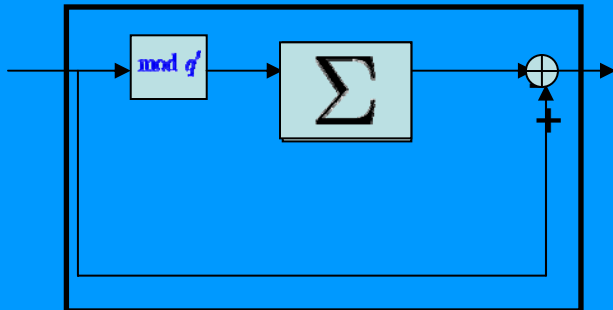
- Programmed level is a random function of the program pulse
- Programming to exact levels requires many write/read iterations
- Allowing AIM errors speeds up programming or avoids costly erases
- Optimization problem: Budget AIM errors to minimize program time
(experimental study on Floating Gate Arrays with Georgia Tech.)

Conclusion



$$|C| \cdot \sum_{i=0}^t \binom{n}{i} \ell^i = q^n$$

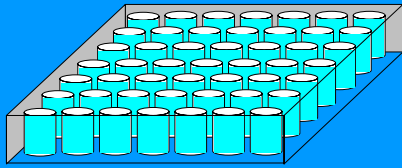
$$C = \{x \in Q^n : x \bmod (\ell + 1) \in \Sigma\}$$



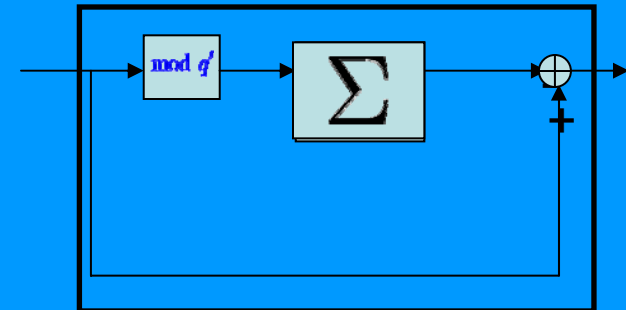
0	000
1	001
2	002
3	012
4	...



Questions?



$$|C| \cdot \sum_{i=0}^t \binom{n}{i} \ell^i = q^n$$



$$C = \{x \in Q^n : x \bmod (\ell + 1) \in \Sigma\}$$



0	000
1	001
2	002
3	012
4	...